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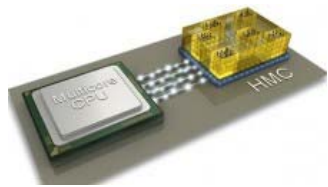
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Posts Tagged «hybrid memory cube»



**Micron's 320GB/sec Hybrid Memory Cube comes to market in 2013, threatens to finally kill DDR SDRAM** April 3, 2013 at 8:18 am

The Hybrid Memory Cube Consortium, which consists of such silicon luminaries as Micron, Samsung, and IBM (but not Intel), has finally finished hammering out the Hybrid Memory Cube 1.0 standard. The HMC is a complete paradigm shift away from conventional DDR1/2/3 SDRAM, offering up to 15 times the performance of DDR3, while using 70% less energy. Just to whet your appetite, HMC 1.0 has a max bandwidth of 320GB/sec to a nearby CPU or GPU – PC3-24000 DDR3 SDRAM, on the other hand, maxes out at just 24GB/sec.

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